

10/531050

IDS - 04/12/2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JC13 Rec'd PCT/PTO 12 APR 2005

Appl. No. : TBD
Applicant : Shunsuke Hirano et al.
Filed : Herewith
Title : MODULATOR AND CORRECTION METHOD THEREOF

Int'l. Application No. : PCT/JP2004/000061
Int'l. Filing Date : January 8, 2004

Conf. No. : TBD
TC/A.U. : TBD
Examiner : TBD

Customer No. : 000,116
Docket No. : 38028

INFORMATION DISCLOSURE STATEMENT

Mail Stop PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

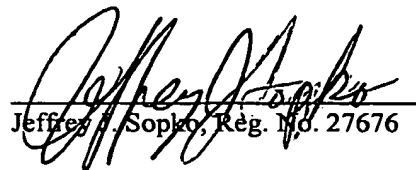
Sir/Madam:

In accordance with 37 C.F.R. § 1.98, applicant is submitting herewith Form PTO-1449 listing references for consideration by the Examiner. A copy of the International Search Report is enclosed. Copies of the references cited in the International Search Report are not included because they were previously sent by the International Bureau.

Respectfully submitted,

PEARNE & GORDON LLP

By:


Jeffrey J. Sopko, Reg. No. 27676

1801 East 9th Street
Suite 1200
Cleveland, Ohio 44114-3108
(216) 579-1700

Date: April 12, 2005

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10/531050

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET 38028		SERIAL NO. TBD	
INFORMATION DISCLOSURE CITATION BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)				APPLICANT: Shunsuke Hirano et al.		GROUP ART UNIT: TBD	
				FILING DATE: Herewith			
Page 1 of 1							

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Subclass	Filing Date
DM	A	6,008,703	Perrott et al.			
	B					
	C					
	D					
	E					
	F					
	G					
	H					
	I					
	J					
	K					
	L					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Class	Subclass	Translation
DM	M	6-326518	11/1994	JP	Cited in ISR
DM	N	2001-156629	6/2001	JP	Cited in ISR
DM	P	9-289447	11/1997	JP	Cited in ISR
DM	Q	2002-207527	7/2002	JP	Cited in ISR

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

R	
S	
T	

Examiner: /David Mis/ (09/28/2006)	Date Considered 09/28/2006
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*Examiner: Initial if reference considered, regardless of whether citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

JC13 Rec'd PCT/PTO 12 APR 2005

TFW 2817

IDS 84/14/2006



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/531,050
Applicant : Shunsuke Hirano et al.
Filed : April 12, 2005
Title : "MODULATOR AND CORRECTION METHOD
THEREOF"

Confirmation No. : 5055
TC/A.U. : 2817
Examiner : TBD

Customer No. : 116
Docket No. : 38028

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Va. 22313-1450

Sir/Madam:

In accordance with Rule 56, applicants are aware of the publications listed in the European Search Report (ESR, copy enclosed) of a corresponding European application and in the enclosed copy of Patent Office Form 1449. A copy of each of the publications is enclosed herewith.

The undersigned hereby certifies that each item of information contained in this Supplemental Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Supplemental Information Disclosure Statement.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

Jeffrey J. Sopko

Name of Attorney for Applicant(s)

April 11, 2006

Date

Signature of Attorney

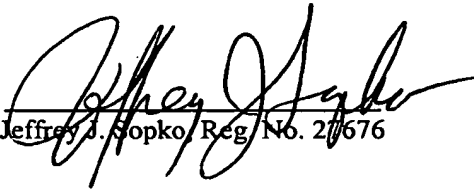
Application No.: 10/531,050
April 11, 2006
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If there are any fees required by this communication, please charge such fees to our
Deposit Account No. 16-0820, Order No. 38028.

Respectfully submitted,

PEARNE & GORDON LLP

By:



Jeffrey J. Sopko Reg No. 27676

1801 East 9th Street
Suite 1200
Cleveland, Ohio 44114-3108

(216) 579-1700

April 11, 2006

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 38028	SERIAL NO. 10/531,050
SUPPLEMENTAL INFORMATION DISCLOSURE CITATION BY APPLICANT <i>(USE SEVERAL SHEETS IF NECESSARY)</i>				APPLICANT: Shunsuke Hirano et al.	
				FILING DATE: April 12, 2005	GROUP ART UNIT: 2817

OFFICE
 APR 17 2005
 PATENT & TRADEMARK OFFICE

U.S. PATENT DOCUMENTS							
Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date if Appropriate
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						

FOREIGN PATENT DOCUMENTS							
		Document No.	Date	Country	Class	Subclass	Translation
DM	L	2 809 890	12/2001	FRANCE			Cited on ESR-English Abstract
	M						
	N						
	P						
	Q						

OTHER REFERENCES <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>		
DM	R	Perrott et al., "A 27-mW CMOS Fractional -N Synthesizer Using Digital Compensation for 2.5-Mb/s GFSK Modulation, 12/1997, pages 2048-2060 (Cited on ESR-English Text)
	S	
	T	

Examiner: /David Mis/ (09/28/2006)	Date Considered 09/28/2006
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